L Number	Hits	Search Text	DB	Time stamp
1	339	compar\$3 and (read-in with read-out)	USPAT;	2003/08/22 15:00
1			US-PGPUB;	
	[EPO; JPO;	
			DERWENT;	
			IBM_TDB	
2	205647		USPAT;	2003/08/22 15:00
		read-out))	US-PGPUB;	•
			EPO; JPO;	
			DERWENT;	
	1400=::	Floor manner	IBM_TDB	2002/02/22 == ==
3	1480540	flash memory	USPAT;	2003/08/22 15:00
	1		US-PGPUB;	
			EPO; JPO; DERWENT;	i
			IBM TDB	i
4	276	(compar\$3 and (read-in with read-out)) and	USPAT;	2003/08/22 15:01
*	2,0	(flash memory)	US-PGPUB;	2003,00,22 13:01
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
5	2555236	failure memory cell\$1	USPAT;	2003/08/22 15:02
			US-PGPUB;	
			EPO; JPO;	
1			DERWENT;	
			IBM_TDB	
6	273		USPAT;	2003/08/22 15:03
		(flash memory)) and (failure memory	US-PGPUB;]
		cel1\$1)	EPO; JPO;	
			DERWENT;	
7	1138349	failure counter\$1	IBM_TDB USPAT;	2003/08/22 15:03
'	1130343	Larrare Counterys	US-PGPUB;	4003/00/22 15:03
			EPO; JPO;	
]			DERWENT;	
		\	IBM TDB	
8	141	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	USPAT;	2003/08/22 15:03
		(flash memory)) and (failure memory	US-PGPUB;	
		cell\$1)) and (failure counter\$1)	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
9	9780	discontinuing and (test operation with	USPAT;	2003/08/22 15:05
		number memory cell\$1 reach\$3 predetermined number)	US-PGPUB;	
		number)	EPO; JPO;	
			DERWENT;	
10	ი	((((compar\$3 and (read-in with read-out))	USPAT;	2003/08/22 15:06
		and (flash memory)) and (failure memory	US-PGPUB;	2003,00,22 13.00
		cell\$1)) and (failure counter\$1)) and	EPO; JPO;	
		(discontinuing and (test operation with	DERWENT;	
		number memory cell\$1 reach\$3 predetermined	IBM TDB	
		number))	_	
11	4015030	discontinuing with test operation	USPAT;	2003/08/22 15:06
		•	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	401700:		IBM_TDB	
13	4015834	stopping with test operation	USPAT;	2003/08/22 15:08
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
14	1887742	(discontinuing with test operation) and	IBM_TDB USPAT	2003/00/22 15:00
- •	2007742	(stopping with test operation)	OSFAI	2003/08/22 15:09
15	128	((((compar\$3 and (read-in with read-out))	USPAT	2003/08/22 15:09
-		and (flash memory)) and (failure memory		
		cell\$1)) and (failure counter\$1)) and		
		((discontinuing with test operation) and		
		(stopping with test operation))		
16	48086	(replacing and (defect\$3 memory with spare	USPAT	2003/08/22 15:16
		memory))		

17	25	(((((compar\$3 and (read-in with read-out))	USPAT	2003/08/22 15:16
		and (flash memory)) and (failure memory		
		cell\$1)) and (failure counter\$1)) and		
		((discontinuing with test operation) and		
		(stopping with test operation))) and		ĺ
		((replacing and (defect\$3 memory with spare		
		memory)))		

Tead-out) US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT;	
2 205647 flash memory and (compar\$3 and (read-in with read-out)) SPAT;	/22 13:00
2 205647 flash memory and (compar\$3 and (read-in with read-out)) DERWENT; IBM TDB USPAT; US-PGPUB; EPO; JPO; JPO; DERWENT; IBM TDB USPAT; US-PGPUB; EPO; JPO; JPO; JPO; JPO; JPO; JPO; JPO; J	
2 205647 flash memory and (compar\$3 and (read-in with read-out)) IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERNEMT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; JPO; JPO; JPO; JPO; JPO; JPO; J	
2 205647 flash memory and (compar\$3 and (read-in with read-out)) USPĀT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPĀT; US-PGPUB; EPO; JPO; JPO; DERWENT; IBM TDB USPĀT; US-PGPUB; EPO; JPO; JPO; DERWENT; IBM TDB USPĀT; US-PGPUB; EPO; JPO; JPO; JPO; JPO; JPO; JPO; JPO; J	
Tead-out) S-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT; US-PGPUB; EPO;	
Separate	/22 15:00
1480540 flash memory DERWENT; IBM TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB EPO; JPO; DE	
1480540 flash memory 18M TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT; US-PGPUB; EPO; JPO; DERW	
3	
4	/22 15 00
4 276 (compar\$3 and (read-in with read-out)) and (flash memory) 5 2555236 failure memory cell\$1 6 273 ((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1 7 1138349 failure counter\$1 8 141 (((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1) and (flash memory)) and (failure memory cell\$1) and (flash memory) and (failure memory cell\$1) and (failure counter\$1) and (failure memory cell\$1) and (failure counter\$1) and (discontinuing and (test operation with number memory cell\$1) and (failure counter\$1) and (flash memory) and (failure memory cell\$1) and (failure memory cell\$1) and (failure memory cell\$1) and (failure memory cell\$1) and (failure counter\$1) and (failure memory cell\$1) and (failure memory cell\$1] and (failure	/22 15:00
DERMENT; IBM_TDB USPĀT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPĀT	
18M_TDB 18M_	
276	
(flash memory)	/22 15:01
5 2555236 failure memory cell\$1 USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT	,
failure memory cell\$1 IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; US-PGPUB	
5 2555236 failure memory cell\$1 USPĀT; US-PGPUB; EPG; JPO; DERWENT; IBM_TDB USPĀT; US-PGPUB; US-PG	
US-PGPUB; EPO; JPO; DERWENT; IBM TDB US-PGPUB; E	
((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DER	/22 15:02
Compar\$3 and (read-in with read-out)) and (flash memory) and (failure memory US-PGPUB; cell\$1) Compar\$3 and (read-in with read-out) Compar\$3 and (failure memory cell\$1) Compar\$3 and (failure memory cell\$1) Compar\$3 and (failure memory cell\$1) Compar\$3 and (read-in with read-out) Compar\$3 and (failure memory cell\$1) Compar\$3 and (failure memory cell\$1)	
((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1) ((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1) ((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1) and (failure memory cell\$1) (((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1) and (failure memory cell\$1) (((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1) ((((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1) and (failure memory cell\$	
((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1) [7] 1138349	
(flash memory)) and (failure memory cell\$1) (flash memory)) and (failure memory cell\$1) (graph of the properties of	/22 15 22
cell\$1) failure counter\$1 [EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT; US-PGPUB; IBM_TDB USPAT; IB	/22 15:03
DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; IBM_TDB	
7 1138349 failure counter\$1	
7	
US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; (flash memory)) and (failure memory cell\$1)) and (failure counter\$1) 9 9780 discontinuing and (test operation with number memory cell\$1 reach\$3 predetermined number) 10 0 ((((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1)) and (failure memory discontinuing and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1)) and (failure counter\$1)) and (discontinuing and (test operation with number memory cell\$1 reach\$3 predetermined US-PGPUB; EPO; JPO; DERWENT; IBM_TDB 2003/08 2003/08 2003/08 10 0 ((((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1)) and (failure counter\$1)) and (discontinuing and (test operation with number memory cell\$1 reach\$3 predetermined US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	/22 15:03
8 141 (((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1)) and (failure counter\$1) US-PGPUB; number) 9 9780 discontinuing and (test operation with number memory cell\$1 reach\$3 predetermined number; EPO; JPO; DERWENT; IBM_TDB (SPO; JPO; DERWENT; IBM_TDB) 10 0 ((((compar\$3 and (read-in with read-out)) US-PGPUB; Cell\$1)) and (failure memory cell\$1 reach\$3 predetermined (IS-PGPUB; Cell\$1)) and (failure memory cell\$1) and (failure memory cell\$2) and (failure memory cell\$3) and (failure memory cell\$3) and (failure memory cell\$4) and (failure memory ce	, 22 13.03
8 141 (((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1)) and (failure counter\$1) (US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; Cell\$1)) and (failure counter\$1)) and EPO; JPO; (discontinuing and (test operation with number memory cell\$1 reach\$3 predetermined IBM_TDB	
141 (((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1)) and (failure counter\$1) 9 9780 discontinuing and (test operation with number memory cell\$1 reach\$3 predetermined number) 10 0 ((((compar\$3 and (read-in with read-out)) uspar; and (flash memory)) and (failure memory cell\$1) and (failure memory cell\$1) and (failure memory cell\$1) and (failure counter\$1)) and (discontinuing and (test operation with number memory cell\$1 reach\$3 predetermined uspar; lbM_TDB cell\$1)) and (failure counter\$1)) and perwent; number memory cell\$1 reach\$3 predetermined lbM_TDB	
(flash memory)) and (failure memory cell\$1)) and (failure counter\$1) 9 9780 discontinuing and (test operation with number memory cell\$1 reach\$3 predetermined number) 10 0 ((((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1)) and (failure counter\$1)) and cell\$1)) and (failure counter\$1)) and cepo; JPO; (discontinuing and (test operation with number memory cell\$1 reach\$3 predetermined iBM_TDB	
cell\$1)) and (failure counter\$1) 9 9780 discontinuing and (test operation with number memory cell\$1 reach\$3 predetermined number) 10 0 ((((compar\$3 and (read-in with read-out)) uspat; and (flash memory)) and (failure memory cell\$1)) and (failure memory cell\$1)) and (failure counter\$1)) and (discontinuing and (test operation with number memory cell\$1 reach\$3 predetermined ibm_TDB	/22 15:03
9 9780 discontinuing and (test operation with number memory cell\$1 reach\$3 predetermined number) 10 0 ((((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1)) and (failure counter\$1)) and (discontinuing and (test operation with number) DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; US-PGPUB; EPO; JPO; OERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	
9 9780 discontinuing and (test operation with number memory cell\$1 reach\$3 predetermined number) 10 0 ((((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1)) and (failure counter\$1)) and (discontinuing and (test operation with number tempory cell\$1 reach\$3 predetermined 1	
9 9780 discontinuing and (test operation with number memory cell\$1 reach\$3 predetermined number) 10 0 ((((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1)) and (failure counter\$1)) and (discontinuing and (test operation with number memory cell\$1 reach\$3 predetermined 10 prat; US-PGPUB; EPO; JPO; US-PGPUB; EPO; JPO; DERWENT; DERWENT; number memory cell\$1 reach\$3 predetermined 10 prat; US-PGPUB; EPO; JPO; DERWENT; DERWEN	
number memory cell\$1 reach\$3 predetermined US-PGPUB; EPO; JPO; DERWENT; IBM_TDB 10 0 ((((compar\$3 and (read-in with read-out)) USPAT; US-PGPUB; and (flash memory)) and (failure memory cell\$1)) and (failure counter\$1)) and EPO; JPO; (discontinuing and (test operation with DERWENT; number memory cell\$1 reach\$3 predetermined IBM_TDB	/22 15.05
number) 10 0 ((((compar\$3 and (read-in with read-out)) USPAT; US-PGPUB; cell\$1)) and (failure memory cell\$1 reach\$3 predetermined IBM_TDB	/22 15:05
DERWENT; IBM_TDB USPAT; and (flash memory)) and (failure memory US-PGPUB; cell\$1)) and (failure counter\$1)) and (discontinuing and (test operation with number memory cell\$1 reach\$3 predetermined IBM_TDB	
10 0 ((((compar\$3 and (read-in with read-out)) and (flash memory)) and (failure memory cell\$1)) and (failure counter\$1)) and (discontinuing and (test operation with number memory cell\$1 reach\$3 predetermined IBM_TDB	
0 ((((compar\$3 and (read-in with read-out)) uspat; and (flash memory)) and (failure memory cell\$1)) and (failure counter\$1)) and EPO; JPO; (discontinuing and (test operation with number memory cell\$1 reach\$3 predetermined IBM_TDB	
and (flash memory)) and (failure memory US-PGPUB; cell\$1)) and (failure counter\$1)) and EPO; JPO; (discontinuing and (test operation with DERWENT; number memory cell\$1 reach\$3 predetermined IBM_TDB	/22 15:06
cell\$1)) and (failure counter\$1)) and EPO; JPO; (discontinuing and (test operation with DERWENT; number memory cell\$1 reach\$3 predetermined IBM_TDB	-
number memory cell\$1 reach\$3 predetermined IBM_TDB	
number))	
111 1 4017020 41-2-2-1-2-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1	100 0
	/22 15:06
US-PGPUB;	
EPO; JPO; DERWENT;	
IBM TDB	
	/22 15:08
US-PGPUB;	,
EPO; JPO;	
DERWENT;	
IBM_TDB	
14 1887742 (discontinuing with test operation) and USPAT 2003/08	/22 15:09
(stopping with test operation)	
	/22 15:09
and (flash memory)) and (failure memory	
cell\$1)) and (failure counter\$1)) and	
((discontinuing with test operation) and	
(stopping with test operation)) 16 48086 (replacing and (defect\$3 memory with spare USPAT 2003/08	/22 15 16
16	/22 15:16

17	25	(((((compar\$3 and (read-in with read-out))	USPAT	2003/08/22 15:52
		and (flash memory)) and (failure memory		1
		cell\$1)) and (failure counter\$1)) and		
		((discontinuing with test operation) and		
		(stopping with test operation))) and		
		((replacing and (defect\$3 memory with spare		
		memory)))		
18	746	714/719	USPAT	2003/08/22 15:52
19	416	(714/719).CCLS.	USPAT	2003/08/22 16:37